

4.5 GHz Network Node Real-Time Spectrum Analyzer

NXN-45

Product Brochure V0.2

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- 9 kHz~4.5 GHz real-time spectrum analyzer
- Integrated 100 kHz-6.3 GHz analog signal generator (option)
- Equipped with preamplifier, DANL: -160 dBm/Hz, residual response: -110 dBm
- 1GHz phase noise: -110 dBc/Hz@10 kHz offset
- Bandwidth: 6.25 MHz, spectrum sweep speed > 69 GHz/s
- Weight: 650 grams (core module) size: 167 x 117 x 28 mm, power consumption: 14 W
- 1000M/100M Ethernet interface
- Build-in multimode GNSS
- Provides 1PPS, latitude and longitude information and timestamp
- Highly compatible API interfaces and SASstudio4 GUI
- Remote master of ARM and x86 processor are supported
- Linux and Windows are supported
- Operating temperatures range from -20 °C/-40 °C to 65 °C (option)
- Built-in OCXO (option) or GNSS disciplined OCXO (option)

NXN-45 Technical Specifications * (typical value)

Indicator test basis Hardware Version: R3 API: 0.50.1 FPGA: 0.50.0 MCU: 0.50.2 SAS4: 4.1.50.40

Frequency				
Frequency Range	9 kHz~4.5 GHz			
Initial Frequency Accuracy	<1 ppm, supporting program manual correction			
Reference Clock	Internal or external, program-controlled switching, internal TCXO aging<1 ppm/year, temperature drift≤1 ppm; internal OCXO (option), temperature drift≤0.15 ppm			
Disciplined GNSS	Support external GNSS (option) disciplines and recalculates built-in reference clock			
Spectrum Purity				
SSB Phase Noise	dBc/Hz			
Carrier Frequency	500 MHz	1 GHz	3 GHz	4.5 GHz
1 kHz	-109.3	-104.4	-96.5	-90.2
10 kHz	-117.4	-111.3	-100.3	-98.4
100 kHz	-117.2	-109.3	-98.5	-96.6
1 MHz	-131.2	-129.5	-124.4	-119.6
Residual Response Spurious Rejection on dBm, RBW =1 kHz Positive Peak Detector	Frequency Range	R.L.=0 dBm	R.L.=-20 dBm	R.L.=-50 dBm
	100 kHz~100 MHz	<-85	<-105	<-100
	100 MHz~4.5 GHz	<-85	<-105	<-120
Residual Response Spurious Rejection off dBm; RBW =1 kHz	100 kHz~100 MHz	<-85	<-95	<-115
	100 MHz~4.5 GHz	<-85	<-95	<-100
Image Frequency Suppression	>90 dBc (spurious rejection on), >35 dBc (spurious rejection off, typical value)			
Local Oscillator Related Spurious	<-65 dBc (Offset Center Frequency +/- (N/M)*125 MHz, N/M = 1,2,3,4,5...)			
Linearity				
IIP3 (dBm) 2 MHz step -6 dBfs/Tone	1 GHz		3 GHz	4.5 GHz
R.L.= 20 dBm	48.7		41.8	37.6
R.L.= 0 dBm	27.6		27.6	24.5
R.L.= -20 dBm	9.2		8.7	4.6
R.L.= -50 dBm	-28.1		-26.8	-28.3
IIP2 (dBm) 2 MHz step -6 dBfs/Tone	1 GHz		2 GHz	3 GHz
R.L.= 20 dBm	>77		>82	>82
R.L.= 0 dBm	>77		>77	>77
R.L.= -20 dBm	>67		>67	>67
R.L.= -50 dBm	>62		>62	>67
Signal Processing				
Analysis Bandwidth	Maximum 6.25 MHz, Decimate Factor:1			
IQ Data	7.8125 MSPS Decimate factor: 1,2,4,8,16,32,64,128,256 supported (FPGA)			
Storage Depth	The built-in memory depth is 128 Mbytes Supports continuous and uninterrupted storage when the data generation rate is less than the bus bandwidth, and the storage depth is only limited by the hard disk capacity			
External trigger response	Maximum frequency response 500 times/sec			
Analog IF output	Not supported			
Amplitude				
Maximum safe input power	26 dBm	30 MHz~4.5 GHz the preamplifier off (R.L. ≥ 0 dBm)		

(CW)	10 dBm	100 kHz~30 MHz or preamplifier on (R.L. <0 dBm)		
Maximum DC Voltage	+/-15 VDC			
Display Range	DANL~26 dBm			
Amplitude Accuracy	+/- 1.5 dB			
IF in-band spectrum ripple	+/- 1.75 dB (100 MHz analog IF bandwidth)			
Reference level (R.L.)	-50 dBm~23 dBm			
RF Preamplifiers	Frequency converting bands (frequency ≥ 30 MHz) are equipped with preamplifier that can be set as automatically turn on or forcibly turn off			
VSWR	<1.7:1	30 MHz~4.5 GHz (R.L. ≥ 10 dBm)		
	<2.0:1	30 MHz~4.5 GHz (R.L. ≥ 0 dBm)		
	<2.5:1	30 MHz~4.5 GHz (R.L. ≥ -40 dBm)		
Display Average Noise Level (DANL) dBm/Hz RBW=10 kHz RMS detector	Frequency Range	R.L.= 0 dBm (IFGainGrade = 3)	R.L.=-20 dBm (IFGainGrade = 3)	R.L.=-50 dBm (IFGainGrade = 3)
	9 kHz	<-103.6	<-114.2	<-116.2
	100 kHz~100 MHz	<-131.3	<-136.3	<-134.8
	100 MHz~3.0 GHz	<-130.3	<-148.4	<-157.3
3.0 GHz~4.5 GHz	<-124.4	<-141.5	<-158.4	
Standard Spectrum Analysis				
Detector	Positive peak, Negative peak, Sampling, Average, RMS, Max Power			
RBW	0.1 Hz~1 MHz			
VBW	0.1 Hz~1 MHz			
Trace Function	Sample, PosPeak, NegPeak, Local average, Maximum hold, Minimum hold, Average			
Data Chart	SAStudio4 software provides regular spectrum, waterfall chart, and historical trace			
Sweep speed - Standard Spectrum Analysis	75.2 GHz/s	FPGA	RBW≥250 kHz, B-Nuttall window, spurious rejection: Standard	
	35.2 GHz/s	FPGA	RBW≥250 kHz, B-Nuttall window, spurious rejection: Enhanced	
	5.4 GHz/s	FPGA	RBW=30 kHz, B-Nuttall window, spurious rejection: Enhanced	
	2.2 GHz/s	CPU	RBW=1 kHz, B-Nuttall window, spurious rejection: Enhanced	
Detection Analysis/Zero Span				
Highest Time Resolution	128 ns			
Maximum Analysis Bandwidth	6.25 MHz			
Trace Detection	Positive peak, Negative peak, Sampling, Average, RMS, Max Power			
Real Time Spectrum Analysis				
FFT Analysis	Variable point FFT engine implemented by FPGA. frame rate compression and trace detection are supported. There is strictly no gap and overlap between FFT frames.			
	FFT refresh rate= $10^9 \text{ ns}/(N * D * 128 \text{ ns})$; POI = $2 * N * D * 128 \text{ ns}$ N is the number of FFT points (2048,1024,512,256,128,64,32), and D is the decimate factor (1, 2, 4, 8,...)			
	Typical Settings	FFT Refresh Rate		POI
	N = 2048, D = 1	3,814 times/sec		524.288 us
N = 32, D = 1	244,140 times/sec		8.192 us	
Real-time Analysis Bandwidth	6.25 MHz			
Window Function	B-Nuttall, FlatTop			
RBW	920 kHz-3.59 kHz (FlatTop window); 488 kHz~1.90 kHz (B-Nuttall), 9 grades for each window type			
Amplitude Resolution	0.75 dB			
Signal generator (option)				
Frequency range	100 kHz~6.3 GHz, 10 Hz for each step			
Power range	-50 dBm~0 dBm, 0.25 dB for each step			

VSWR	<2.0:1	30 MHz~6.3 GHz			
Non-harmonic spurs	<-50 dBc				
Harmonic wave	100kHz~30MHz	30MHz~1.6GHz	1.6GHz~3GHz	3GHz~3.2GHz	3GHz~6.3GHz
Second harmonic	<-10 dBc	<-10 dBc	<-20 dBc	<-20 dBc	<-20 dBc
Third harmonic and above	<-10 dBc	<-10 dBc	<-20 dBc	<-20 dBc	<-20 dBc
Signal leakage to receiver	100 kHz~30 MHz		>90 dBc		
	30 MHz~3 GHz		>80 dBc		
	3 GHz~6.3 GHz		>70 dBc		
General					
Input And Output	Power Supply	Type-C (1) PD (QC3.0) 12V 2A or 9V 2A			
	Data	RJ45 1000Mbps x1, 100Mbps x1			
	RF input	SMA (F)(1), Input impedance 50 Ω			
	RF output	SMA (F)(2), Input impedance 50 Ω			
	External reference clock input	MCX (F)(1), amplitude≥1.5Vpp, input impedance 330 Ω			
	External reference clock output	Not support			
	External trigger input	MMCX (F)(1), 3.3V CMOS, input: high impedance			
	External trigger output	MMCX (F)(2), 3.3V CMOS			
	Analog IF Output	Not support			
	GNSS antenna	MMCX (F)(3)			
	4G module antenna	MMCX (F)(4)			
	General USB2.0	Type-C (2)			
Power Consumption	Peak: 14 W, typical: 12 W				
Operating Temperature (ambient temperature/device core temperature)	0~50 °C/0~70 °C (Standard temperature class)				
	-20~65 °C/ -20~85 °C (Extended Temperature Class Option) (plastic enclosure and fan not included)				
	-40~65 °C/ -40~85 °C (Wide Temperature Class Option) (plastic enclosure and fan not included)				
Storage Temperature (ambient temperature)	-20~70 °C				
	-40~85 °C (Extended temperature class and wide temperature options) (plastic enclosure and fan not included)				
Weight and size	Size: 167 x117 x28 mm, weight:650 g (Including protective case and structural fittings, including connector length)				
Accessories	Flash disk×1, USB cable×2, power adaptor×1				

*The typical values of the indicators are applicable for the following conditions: (1) Start up and warm up for 20 minutes; (2) Ambient temperature 25 °C (core temperature 50 °C); (3) Standard sweep mode-Spurious rejection on; (4) 6.25 MHz analysis bandwidth and IFGainGrade=3; (5) The user shall provide the necessary heat dissipation conditions to ensure that the ambient temperature and the core temperature of the equipment are within the rated range at the same time.

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Code name	Option	Explanation
01	Built-in OCXO reference clock (hardware)	Providing a reference clock with better stability than the standard configuration, with a temperature drift of <math><0.15\mu\text{m}</math>, increasing the overall power consumption by 0.8 W
05	Build-in GNSS disciplined OCXO reference clock (hardware opt.)	Providing GNSS disciplined reference clock and 1PPS, increasing the overall power consumption by 1.1W.
06	Build-in premium GNSS (hardware opt.)	Providing improved positioning and timing capabilities.
09	Build in 4G data module (hardware opt.)	Providing the physical connection to the 4G connection
20	Extended temperature class (hardware opt.)	- 20~65 °C/- 20~85 °C(Extended temperature class opt.)
21	Wide temperature class (hardware opt.)	- 40~65 °C/- 40~85 °C(Wide temperature class opt.)

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